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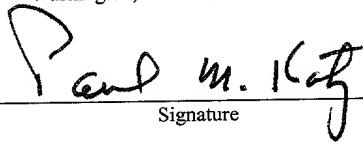
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**APPLICATION FOR LETTERS PATENT**

**FOR**

**PROGRAMMABLE IDENTIFICATION IN  
A COMMUNICATIONS CONTROLLER**

**Inventors: Mark Palmer and Steven Eric Schlanger**

**ASSIGNEES: Microchip Technology Incorporated  
and Aegis Technologies L.L.C.**

**PROGRAMMABLE IDENTIFICATION IN  
A COMMUNICATIONS CONTROLLER**

**Field of the Invention**

The present invention relates generally to communication controllers, and more particularly to an integrated circuit (IC) communication protocol controller having an identification data string which may be changed without disturbing other programmed functions from a generic identification data string to a unique identification data string customized for a specific purpose.

**Background of the Invention Technology**

Communications controllers may be used whenever two or more separate devices must communicate together by using a certain communications protocol. Generally, data is transferred (sent and received) between the two or more separate devices by sequential digital signals, e.g., serially transmitted digital packets. Numerous communications protocol are being used for both wired and wireless communications links between the two or more devices, e.g., USB, IEEE 1451, J-1850, DALI, LIN, ethernet, and wireless standards such as radio frequency, e.g., Bluetooth, and infrared transmission, e.g., IrDA.

For example, but not limited to, wireless communications links such as infrared (IR) between two or more devices require a robust communications protocol. The devices at each end of an IR link may move freely within range and out of range. The IR link may be broken in the middle of a transmission, or may suffer interference from external light sources. The Infrared Data Association (IrDA) is an organization which promotes an IR standard for interoperability of wireless IR links between various

manufacturers devices. The IrDA defines a set of specifications, or protocol stack, that provides for the establishment and maintenance of an IR link so that error free communication is possible. The IrDA Standard includes three mandatory specifications: the Physical Layer, Link Access Protocol (IrLAP), and Link Management Protocol (IrLMP). The first version of the IrDA specification, Version 1.0, provided for communication at data rates up to 115.2 Kb/s. Version 1.1 extended the data rates up to 4 Mb/s, while maintaining backward compatibility with Version 1.0 compliant products.

All IrDA Specifications are incorporated by reference herein for all purposes.

When two IrDA compliant devices establish a connection the process is analogous to connecting two devices with serial ports using a cable. An IrDA connection is limited to half-duplex operation because the infrared transmitter and receiver (transceiver) cannot transmit and receive at the same time.

Whenever two IrDA devices come within optical infrared range, they have to recognize each other. The objective is that one IrDA device has some task to accomplish and the other IrDA device has a resource needed to accomplish this task. For example, when an IrDA equipped laptop computer needs to print a document the laptop computer will be brought within optical range of an IrDA equipped printer. In this case the laptop computer has a task to be performed (print a document) and the printer has the resource to perform the task. The laptop computer is the primary IrDA device and the printer is the secondary IrDA device.

Wired communications links (network) also must provide robust and secure data transfer between devices. Typically devices communicating over wired and/or wireless communications links have different purposes, e.g., printers, camera, keyboard, disk storage, etc. Different brands and models of devices are also frequently found on the communication links. It would be advantageous to be able to easily and rapidly identify characteristics and capabilities of these devices.

What is needed are cost effective and easy to implement ways of associating device identification data strings with respective devices and supplying the necessary logic circuits therefor.

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#### SUMMARY OF THE INVENTION

The invention overcomes the above-identified problems as well as other shortcomings and deficiencies of existing technologies by providing hardware and software methods, system and apparatus for programming and supplying an identification code (identification data string) for any wired or wireless connected devices. According to an exemplary embodiment of the invention, a device comprises a communications controller adapted for the type of wired or wireless communications link that the device is functionally in data communications therewith. The communications controller further comprises a programmable non-volatile memory, e.g., an electrically erasable programmable read only memory (EEPROM), flash memory, etc. A unique identification data string may be programmed into the EEPROM before manufacture of the device or a standard generic pre-programmed identification string may be initially

programmed and then the unique identification string may be programmed into the EEPROM at a later time.

In another exemplary embodiment, an IrDA compliant communications controller having an IrDA protocol stack and a non-volatile memory that may be programmed with  
5 a generic or unique identification data string before assembly into an IrDA compliant system, or may be programmed with the unique identification data string after assembly and integration in the IrDA compliant system. The non-volatile memory of the IrDA communications controller may be adapted to store a generic identification string at the time of manufacture. This generic identification string may be used without modification  
10 or may be easily changed either before or after assembly of the IrDA communications controller into the IrDA compliant system.

In addition, it is contemplated and within the scope of the present invention that the identification data string may be changed (upgraded) whenever an upgrade is made to the associated device. According to an exemplary embodiment of the invention, the  
15 identification string may be stored in user accessible read/write non-volatile memory locations.

The communications controller may be for example, but not limited to, a microcontroller, a microprocessor, a programmable logic array (PLA), an application specific integrated circuit (ASIC), digital logic and the like.

In accordance with an exemplary embodiment of the present invention, an apparatus for storing an identification data string used in identifying characteristic of a device in a communications circuit comprising a communications controller adapted for connection to a device and a communications circuit and a programmable non-volatile memory for storing an identification data string representative of the capabilities of the device. The programmable non-volatile memory may be an electrically erasable and programmable read only memory (EEPROM). The programmable non-volatile memory may also be a programmable flash memory. The communications controller may generate a serial clock rate. The communications circuit may be wireless. The wireless communications circuit may be infrared, Bluetooth or IrDA. The communications circuit may be wired. The wired communications circuit may be ethernet, USB, firewire, DALI, LIN, J-1850 or IEEE-1451.9. Serial data may be transmitted from a UART and serial data may be received by the UART. The programmable non-volatile memory may be programmed with a first identification data string that is generic, and may be adapted to be programmed with a second identification data string that is associated with specific capabilities of the device. The communications controller may comprise a microcontroller, a microprocessor, digital signal processor, a programmable logic array or an application specific integrated circuit.

The present invention is also directed to a method for storing an identification data string used in identifying characteristic of a device in a communications circuit comprising the steps of providing a communications controller adapted for connection to a device and a communications circuit, providing a programmable non-volatile memory

and storing an identification data string representative of the device capabilities. The method further comprising the steps of transmitting serial data with a UART and receiving serial data with the UART. A portion of the transmitted serial data is the identification data string.

5       In addition, the present invention is directed to a system for communications between two or more devices, wherein first device application logic is adapted for sending transmit serial data at a serial clock rate and receiving receive serial data at the serial clock rate. A first communications controller is coupled to said first device application logic, said first communications controller encoding the transmit serial data and decoding the receive  
10      serial data on a communication circuit. Second device application logic adapted for receiving the transmit serial data at the serial clock rate and transmitting the receive serial data at the serial clock rate. A second communications controller coupled to said second device application logic, said communications controller decoding the transmit serial data and encoding the receive serial data on the communication circuit. Said first and second  
15      communications controllers having a first programmable non-volatile memory for storing a first identification data string representative of the capabilities of the first device, said second communications controllers having a second programmable non-volatile memory for storing a second identification data string representative of the capabilities of the second device. Such that the first and second identification data strings are transmitted  
20      and received between the first and second devices over the communications circuit. A first infrared transmitter and first encoder may be coupled to said first communications controller, wherein said first infrared transmitter and first encoder convert first transmit

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pulses from said first communications controller into first infrared light pulses. A second infrared transmitter and second encoder may be coupled to said second communications controller, wherein said second infrared transmitter and second encoder convert second transmit pulses from said second communications controller into second infrared light pulses. A first infrared receiver is adapted for receiving the second infrared light pulses and converting the second infrared light pulses into first receive pulses. A second infrared receiver is adapted for receiving the first infrared light pulses and converting the first infrared light pulses into second receive pulses. The first infrared receiver is coupled to said first communications controller. Said second infrared receiver is coupled to said second communications controller. The first and second device application logic are selected from the group consisting of a microcontroller, a microprocessor, digital signal processor, a programmable logic array and an application specific integrated circuit. The first and second device application logic comprises a central processing unit, a random access memory and a read only memory.

15       A technical advantage of the present invention is a single infrared encoder/decoder that may be used for different applications and with different devices.

Another technical advantage is a unique identification string may be easily programmed into a non-volatile memory of an IrDA compliant device.

20       A feature of the present invention is flexibility in the application of a single design of an encoder/decoder for many different IrDA compliant devices.

Another feature is easily changing an identification string that indicates the type of IrDA device.

Features and advantages of the invention will be apparent from the following description of the embodiments, given for the purpose of disclosure and taken in conjunction with the accompanying drawings.

#### **BRIEF DESCRIPTION OF THE DRAWINGS**

A more complete understanding of the present disclosure and advantages thereof may be acquired by referring to the following description taken in conjunction with the accompanying drawing, wherein:

10       Figure 1 illustrates a schematic functional block diagram of an exemplary embodiment of the present invention;

Figure 2 illustrates a schematic block diagram of an IrDA compliant communications controller adapted for storing a programmable identification string according to the exemplary embodiment of the invention;

15       Figure 3 illustrates a schematic functional block diagram of the encoder/decoder depicted in Figure 2; and

Figure 4 illustrates a schematic functional block diagram of an IrDA compliant system comprising device logic circuits, the IrDA compliant communications controller depicted in Figure 3 and an infrared transmitter and receiver.

While the present invention is susceptible to various modifications and alternative forms, specific exemplary embodiments thereof have been shown by way of example in the drawing and are herein described in detail. It should be understood, however, that the description herein of specific embodiments is not intended to limit the invention to the particular forms disclosed, but on the contrary, the intention is to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

#### DETAILED DESCRIPTION OF SPECIFIC EMBODIMENTS

The present invention is directed to a method, system and apparatus for programming and supplying a unique identification data string in combination with a communications controller. In an exemplary embodiment of the invention, an IrDA compliant communications controller is adapted for storing an identification string in an electrically erasable and programmable read only memory (EEPROM). The communications controller may have an initial generic identification string programmed at the time of manufacture and may subsequently, either before or after shipment from the place of manufacture, have a more specific (unique) identification string programmed so as to more readily and fully identify the IrDA compliant device type, features and other parameters unique to that device in which the communications controller is integrated. It is contemplated and within the scope of the present invention that a unique identification data string may be used in combination with communications controllers adapted for any type of wired or wireless communications protocol.

Referring now to the drawing, the details of an exemplary embodiment of the present invention are schematically illustrated. Like elements in the drawing will be represented by like numbers, and similar elements will be represented by like numbers with a different lower case letter suffix.

5 Referring to Figure 1, depicted is a schematic functional block diagram of an exemplary embodiment of the present invention. A communications controller 102 in combination with an identification data memory 104 provides a device 106 with a unique identification data string. A plurality of devices 106a and 106b may be in communications through associated communications controllers 102a and 102b, respectively, over a data  
10 communications link 108. The data communications link 108 may be either a wired communications circuit, e.g., Ethernet, USB, DALI, LIN, J-1850, IEEE-1451, Firewire, etc., or a wireless communications circuit, e.g., IrDA, Bluetooth, etc. For each of the communications controllers 102a and 102b, an associated identification memory 104a and 104b, respectively, may store either a generic or unique identification data string for each of  
15 the devices 106a and 106b, respectively. The identification data string (generic or unique) may be easily programmed at any time before, during or after manufacture of the device.

Referring to Figure 2, depicted is a schematic block diagram of an IrDA compliant communications controller adapted for a storing a programmable identification string according to an exemplary embodiment of the invention. The IrDA compliant  
20 communications controller is generally represented by the numeral 102 and comprises a processor and arithmetic logic unit (ALU) 206, a random access memory (RAM) 208, a

program memory 210, an identification data electrically erasable and programmable read only memory (EEPROM) 204, a data and address bus 212, a first port input-output (I/O) 214, a second port input-output (I/O) 216, a power-up timer 218, a timing generator 220 and a power-on reset 222. The processor and ALU 206 are controlled by software instructions stored in the program memory 210. The program memory 210 may be on a separate bus 224 than the data and address bus 212. The I/O ports 214 and 216 are adapted for receiving and sending signals to and from the communications controller 102. The identification data EEPROM 204 is adapted for storing IrDA device information that may be unique to the IrDA compliant system in which the encoder/decoder 100 is embedded. The identification data EEPROM 204 may be programmed and reprogrammed at any time with the specific identification string for the IrDA device. The EEPROM 204 may be any type of programmable non-volatile memory (see Figure 1 memory 104), e.g., flash, etc.

The communications controller 102 may be adapted for Data Terminal Equipment (DTE) or for Data Communication Equipment (DCE). Functional operation of the signals at the I/O ports 214 and 216 are more fully described in Table 1 for DTE and Table 2 for DCE.

TABLE 1 (DTE)

PIN NAME	PIN TYPE	BUFFER TYPE	DESCRIPTION
BAUD0	I	ST	BAUD1:BAUD0 specifies the Baud rate of the device.
TXIR	O	-	Asynchronous transmit to IrDA transceiver
RXIR	I	ST	Asynchronous receive from IrDA transceiver
RESET	I	ST	Resets the Device
V <sub>ss</sub>	-	P	Ground reference for logic and I/O Pins
EN	I	TTL	Device Enable. May be used in low power and sleep modes
TX	I	TTL	Asynchronous receive; from Host Controller UART
RX	O	-	Asynchronous transmit; to Host Controller UART
RI	O	-	Ring Indicator. The value on this pin is driven high.
DSR	O	-	Data Set Ready. Indicates that the host controller has completed reset.
DTR	I	TTL	Data Terminal Ready. The value on this pin is ignored.
CTS	O	-	Clear to Send. Indicates that the host controller is ready to receive data.
RTS	I	TTL	Request to Send. Indicates that a Host Controller is ready to receive, and that the host controller must prepare send data if available.
V <sub>DD</sub>	-	P	Positive supply for logic and I/O pins
OSC2	O	-	Oscillator crystal Output
OSC1/CLKIN	I	CMOS	Oscillator crystal input/external clock source input
CD	O	-	Carrier Detect. Indicates that the host controller has established a valid IrDA link with a Primary Device.
BAUD1	I	ST	BAUD1:BAUD0 specify the Baud rate of the device.

TABLE 2 (DCE)

PIN NAME	PIN TYPE	BUFFER TYPE	DESCRIPTION
BAUD0	I	ST	BAUD1:BAUD0 specify the Baud rate of the device.
TXIR	O	-	Asynchronous transmit to IrDA transceiver
RXIR	I	ST	Asynchronous receive from IrDA transceiver
RESET	I	ST	Resets the Device
Vss	-	P	Ground reference for logic and I/O Pins
EN	I	TTL	Device Enable. May be used in low power and sleep modes
TX	I	TTL	Asynchronous receive; from Host Controller UART
RX	O	-	Asynchronous transmit; to Host Controller UART
RI	I	TTL	Ring Indicator. The status of this bit is passed back to the IrDA Primary Device.
DSR	O	-	Data Set Ready. Indicates that the host controller has established a valid IrDA link with a Primary Device. <sup>(2)</sup>
DTR	I	TTL	Data Terminal Ready. The status of this bit is passed back to the IrDA Primary Device.
CTS	O	-	Clear to Send. Indicates that the host controller is ready to receive data.
RTS	I	TTL	Request to Send. Indicates that a Host Controller is ready to receive, and that the host controller must prepare send data is available.
V <sub>DD</sub>	-	P	Positive supply for logic and I/O pins
OSC2	O	-	Oscillator crystal Output
OSC1/CLKIN	I	CMOS	Oscillator crystal input/external clock source input
CD	I	ST	Carrier Detect. The status of this bit is passed back to the IrDA Primary Device.
BAUD1	I	ST	BAUD1:BAUD0 specify the Baud rate of the device.

Legend: TTL = TTL compatible input

I = Input  
 P = Power  
 ST = Schmitt Trigger input with CMOS levels  
 O = Output  
 CMOS = CMOS compatible input

Referring now to Figure 3, a schematic functional block diagram of the communications controller 102 depicted in Figure 2 is illustrated. The communications controller 102 functionally comprises communications controller logic 302, an encoder 304, a decoder 306 and the identification data EEPROM 204. The encoder 304 and decoder 306 may comprise a combination of hardware logic and software program instructions. The exemplary embodiment of the communications controller 102 may be fabricated on a semiconductor integrated circuit 308.

The identification data EEPROM 204 is adapted to store the identification string for the IrDA compliant device. The EEPROM 204 may be programmed at any time for a unique identification string. Initially a generic identification string may be programmed, but once the intended IrDA compliant device is known, or when the communications controller 102 is embedded in a specific IrDA compliant device, the identification string may be easily programmed in the EEPROM 204 with the desired unique identification information.

Referring now to Figure 4, a schematic functional block diagram of an IrDA compliant system comprising IrDA compliant device logic circuits 402 and a UART 404, the communications controller 102 depicted in Figure 2, and an infrared transmitter 406 and an infrared receiver 408. The communications controller 102 is connected between the infrared transmitter 406 and receiver 408 (optical transceiver) and a device 106 (see Figure 1) comprising the product logic circuits 402 and the UART 404. The product logic circuits 302 may be part of an IrDA compliant device, e.g., printer, video camera,

PDA, wireless keyboard, wireless mouse, etc. The communications controller 102, in combination with the infrared transmitter 406 and receiver 408, gives the product IrDA compliant capabilities. The identification data EEPROM 204 supplies the communications controller 102 with the identification string (generic or unique).

- 5       The UART 404 sends transmit serial data (TX) to an input of the encoder 304. The encode 304 encodes the transmit serial data into transmit electrical pulses (TXIR) compatible with IR light transmission. The TXIR pulses are coupled to the IR transmitter 406 for transmission by IR light pulses. The BAUD rate of the light pulses is determined by the communication controller logic 302 (inputs BAUD1 and BAUD0).
- 10       IR light pulses are received and by the IR optical receiver 408. These received IR light pulses are detected by the IR optical receiver 408 and converted into receive electrical pulses (RXIR) which are coupled to the decoder 306. The decoder 306 decodes the received electrical pulses (RXIR) into receive serial data (RX) and formats the receive serial data (RX) to the communications controller logic 302 which sends the RX signals to the UART 404.

- 15       The product logic circuits 402 may be any type of digital circuit having serial digital data communications capabilities, e.g., a microcomputer with a UART 404, a central processing unit (CPU) and UART, a reduced instruction set computer (RISC) or complex instruction set computer (CISC) and a UART, a digital signal processor with a  
20       UART, and the like.

The present invention may function so as to accommodate an infrared standard such as Infrared Data Association (IrDA) Serial Physical Layer Specification, version 1.3 (October 15, 1998) and IrDA Control Specification, final revision (June 30, 1998), both IrDA standards being incorporated by reference herein for all purposes.

5       The invention, therefore, is well adapted to carry out the objects and attain the ends and advantages mentioned, as well as others inherent therein. While the invention has been depicted, described, and is defined by reference to exemplary embodiments of the invention, such references do not imply a limitation on the invention, and no such limitation is to be inferred. The invention is capable of considerable modification,  
10      alternation, and equivalents in form and function, as will occur to those ordinarily skilled in the pertinent arts and having the benefit of this disclosure. The depicted and described embodiments of the invention are exemplary only, and are not exhaustive of the scope of the invention. Consequently, the invention is intended to be limited only by the spirit and scope of the appended claims, giving full cognizance to equivalents in all respects.